

# S-1137 Series

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# 5.5 V INPUT, 300 mA VOLTAGE REGULATOR WITH SOFT-START FUNCTION

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The S-1137 Series, developed using the CMOS technology, is a positive voltage regulator IC which has the low dropout voltage, the high-accuracy output voltage and the soft-start function (300 mA output current).

A 1.0 μF small ceramic capacitor can be used. It operates with low current consumption of 45 μA typ.

The overcurrent protection circuit prevents the load current from exceeding the current capacity of the output transistor. The ON / OFF circuit ensures longer battery life.

Compared to the conventional CMOS technology regulator ICs, various capacitors including small ceramic capacitors can be used for this IC.

Furthermore, small SOT-89-5, SOT-23-5, and SNT-6A(H) packages realize high-density mounting.

#### ■ Features

• Output voltage: 1.2 V to 3.5 V, selectable in 0.05 V step

• Input voltage: 1.7 V to 5.5 V

Output voltage accuracy: ±1.0% (1.2 V to 1.45 V output product : ±15 mV)
 Dropout voltage: 210 mV typ. (2.8 V output product, lout = 300 mA)

Current consumption: During operation: 45 μA typ., 65 μA max.
 During power-off: 0.1 μA typ., 1.0 μA max.
 Output current: Possible to output 300 mA (V<sub>IN</sub> ≥ V<sub>OUT(S)</sub> + 1.0 V)\*1

• Input and output capacitors: A ceramic capacitor of 1.0 μF or more can be used.

• Ripple rejection: 70 dB typ. (f = 1.0 kHz)

Built-in soft-start circuit: Soft-start time: 0.7 ms typ. (C<sub>SS</sub> = 1.0 nF)
 Built-in overcurrent protection circuit: Limits overcurrent of output transistor.

Built-in ON / OFF circuit: Ensures long battery life.

Discharge shunt function "available" / "unavailable" is selectable.
Pull-down function "available" / "unavailable" is selectable.

Operation temperature range:

• Lead-free, Sn 100%, halogen-free\*2

\*1. Attention should be paid to the power dissipation of the package when the output current is large.

 $Ta = 40^{\circ}C$  to  $+85^{\circ}C$ 

\*2. Refer to "■ Product Name Structure" for details.

# ■ Applications

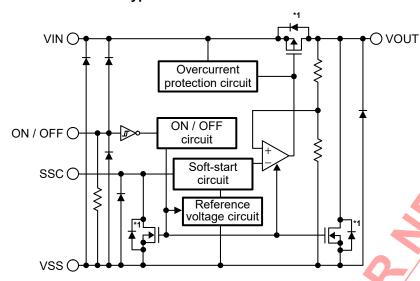
- Constant-voltage power supply for battery-powered device
- Constant-voltage power supply for cellular phone, portable device
- Constant-voltage power supply for digital still camera, TV, DVD recorder

# ■ Packages

- SOT-89-5
- SOT-23-5
- SNT-6A(H)

# **■** Block Diagrams

# 1. S-1137 Series A type

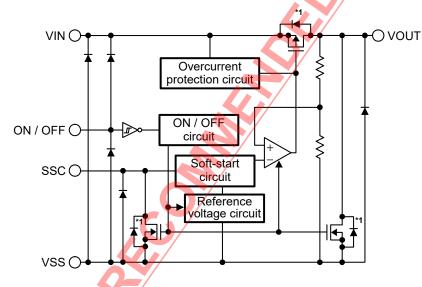


Function Status
ON / OFF logic Active "H"
Discharge shunt function Available
Pull-down resistor Available

\*1. Parasitic diode

Figure 1

# 2. S-1137 Series B type



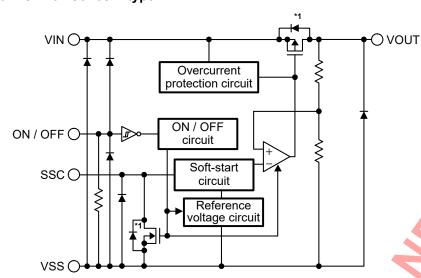
Function	Status
ON / OFF logic	Active "H"
Discharge shunt function	Available
Pull-down resistor	Unavailable

\*1. Parasitic diode

Figure 2

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# 3. S-1137 Series C type

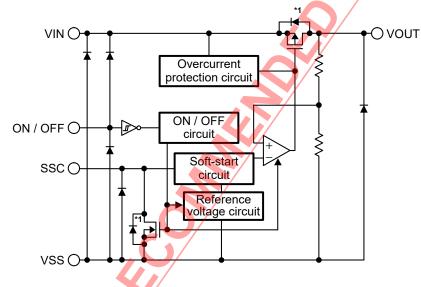


Function	Status
ON / OFF logic	Active "H"
Discharge shunt function	Unavailable
Pull-down resistor	Ayailable

\*1. Parasitic diode

Figure 3

# 4. S-1137 Series D type



Function	Status
ON / OFF logic	Active "H"
Discharge shunt function	Unavailable
Pull-down resistor	Unavailable

\*1. Parasitic diode

Figure 4

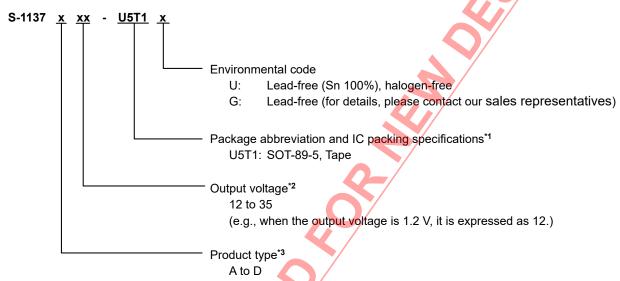
**S-1137 Series** Rev.2.3 00

#### ■ Product Name Structure

Users can select the product type, output voltage, and package type for the S-1137 Series. Refer to "1. Product name" regarding the contents of product name, "2. Function list of product type" regarding the product type, "3. Packages" regarding the package drawings.

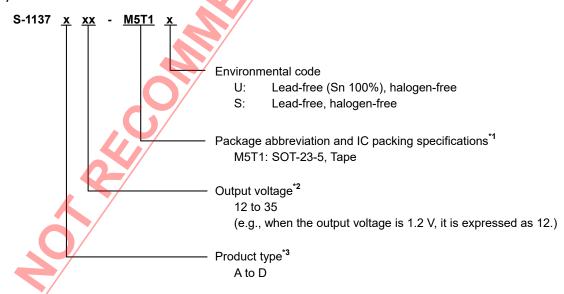
#### 1. Product name

(1) SOT-89-5



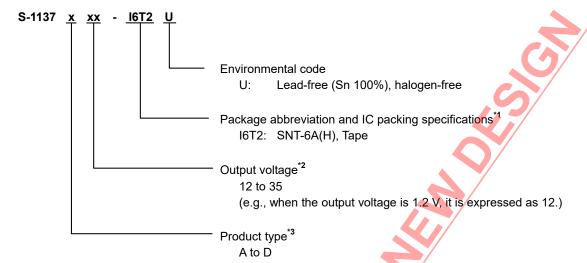
- \*1. Refer to the tape drawing.
- \*2. If you request the product which has 0.05 V step, contact our sales representatives.
- \*3. Refer to "2. Function list of product type".





- **\*1.** Refer to the tape drawing.
- \*2. If you request the product which has 0.05 V step, contact our sales representatives.
- Refer to "2. Function list of product type".

# (3) SNT-6A(H)



- \*1. Refer to the tape drawing.
- \*2. If you request the product which has 0.05 V step, contact our sales representatives.
- \*3. Refer to "2. Function list of product type".

# 2. Function list of product type

Table 1

Product Type	ON / OFF Logic	Discharge Shunt Function	Pull-down Resistor
Α	Active "H"	Available	Available
В	Active "H"	Available	Unavailable
С	Active "H"	Unavailable	Available
D	Active "H"	Unavailable	Unavailable

# 3. Packages

Daakaga Nama	Drawing Code				
Package Name Package		Tape	Reel	Land	
SOT-89-5	UP005-A-P-SD	UP005-A-C-SD	UP005-A-R-SD	_	
SOT-23-5	MP005-A-P-SD	MP005-A-C-SD	MP005-A-R-SD	_	
SNT-6A(H)	PI006-A-P-SD	PI006-A-C-SD	PI006-A-R-SD	PI006-A-L-SD	

#### 4. Product name list

# 4. 1 S-1137 Series A type

ON / OFF logic: Active "H"

Discharge shunt function: Available Pull-down resistor: Available

#### Table 2

Output Voltage	SOT-89-5	SOT-23-5	SNT-6A(H)
1.2 V ±15 mV	S-1137A12-U5T1x	S-1137A12-M5T1y	S-1137A12-I6T2U
1.8 V ±1.0%	S-1137A18-U5T1x	S-1137A18-M5T1y	S-1137A18-I6T2U
2.5 V ±1.0%	S-1137A25-U5T1x	S-1137A25-M5T1y	S-1137A25-I6T2U
3.3 V ±1.0%	S-1137A33-U5T1x	S-1137A33-M5T1y	S-1137A33-I6T2U

Remark 1. Please contact our sales representatives for products other than the above.

- 2. x: G or U
  - y: S or U
- 3. Please select products of environmental code = U for Sn 100%, halogen-free products.

# 4. 2 S-1137 Series B type

ON / OFF logic: Active "H"

Discharge shunt function: Available Pull-down resistor: Unavailable

#### Table 3

		1010 U	
Output Voltage	SOT-89-5	SOT-23-5	SNT-6A(H)
1.2 V ±15 mV	S-1137B12-U5T1x	S-1137B12-M5T1y	S-1137B12-I6T2U
1.8 V ±1.0%	S-1137B18-U5T1x	S-1137B18-M5T1y	S-1137B18-I6T2U
2.5 V ±1.0%	S-1137B25-U5T1x	S-1137B25-M5T1y	S-1137B25-I6T2U
3.3 V ±1.0%	S-1137B33-U5T1x	S-1137B33-M5T1v	S-1137B33-I6T2U

**Remark 1.** Please contact our sales representatives for products other than the above.

- 2. x: G or U
  - y: S or U
- 3. Please select products of environmental code = U for Sn 100%, halogen-free products.

#### 4. 3 S-1137 Series C type

ON / OFF logic: Active "H"

Discharge shunt function: Unavailable Pull-down resistor: Available

#### Table 4

Output Voltage	SOT-89-5	SOT-23-5	SNT-6A(H)
1.2 V ±15 mV	S-1137C12-U5T1x	S-1137C12-M5T1y	S-1137C12-I6T2U
1.8 V ±1.0%	S-1137C18-U5T1x	S-1137C18-M5T1y	S-1137C18-I6T2U
2.5 V ±1.0%	S-1137C25-U5T1x	S-1137C25-M5T1y	S-1137C25-J6T2U
3.3 V ±1.0%	S-1137C33-U5T1x	S-1137C33-M5T1y	S-1137C33-I6T2U

Remark 1. Please contact our sales representatives for products other than the above.

2. x: G or U

y: S or U

3. Please select products of environmental code = U for Sn 100%, halogen-free products.

#### 4. 4 S-1137 Series D type

ON / OFF logic: Active "H"

Discharge shunt function: Unavailable Pull-down resistor: Unavailable

#### Table 5

Output Voltage	SOT-89-5	SOT-23-5	SNT-6A(H)
1.2 V ±15 mV	S-1137D12-U5T1x	S-1137D12-M5T1y	S-1137D12-I6T2U
1.8 V ±1.0%	S-1137D18-U5T1x	S-1137D18-M5T1y	S-1137D18-I6T2U
2.5 V ±1.0%	S-1137D25-U5T1x	S-1137D25-M5T1y	S-1137D25-I6T2U
3.3 V ±1.0%	S-1137D33-U5T1x	S-1137D33-M5T1y	S-1137D33-I6T2U

**Remark 1.** Please contact our sales representatives for products other than the above.

2. x: G or U

y: S or U

3. Please select products of environmental code = U for Sn 100%, halogen-free products.

# ■ Pin Configurations

#### 1. SOT-89-5

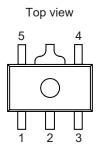


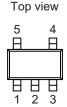
Table 6			
Pin No.	Symbol	Description	
1	VOUT	Output voltage p <mark>i</mark> n	
2	VSS	GND pin	
3	SSC	Pin for soft-start control*1	
4	ON / OFF	ON / OFF pin	
5	VIN	Input voltage pin	

Figure 5

\*1. Connect a capacitor between the SSC pin and VSS pin. The VOUT rising speeds at power-on and at the time when the ON / OFF pin is set to ON can be adjusted according to the capacitance. For details, refer to "

Selection of Capacitor for Soft-start (Css)".

#### 2. SOT-23-5



Pin No. Symbol Description VIN Input voltage pin 2 VSS GND pin 3 ON / OFF ON / OFF pin 4 SSC Pin for soft-start control\*1 5 VOUT Output voltage pin

Table 7

Figure 6

\*1. Connect a capacitor between the SSC pin and VSS pin. The VOUT rising speeds at power-on and at the time when the ON / OFF pin is set to ON can be adjusted according to the capacitance.

For details, refer to "

Selection of Capacitor for Soft-start (Css)".

# 3. SNT-6A(H)



Figure 7

i abic 0			
Pin No.	Symbol	Description	
1	VOUT	Output voltage pin	
2	VSS	GND pin	
3	SSC	Pin for soft-start control*1	
4	ON / OFF	ON / OFF pin	
5	VSS	GND pin	
6	VIN	Input voltage pin	

Table 8

\*1. Connect a capacitor between the SSC pin and VSS pin. The VOUT rising speeds at power-on and at the time when the ON / OFF pin is set to ON can be adjusted according to the capacitance. For details, refer to "

Selection of Capacitor for Soft-start (Css)".

# ■ Absolute Maximum Ratings

Table 9

(Ta = 25°C unless otherwise specified)

Iter	n	Symbol	Absolute Maximum Rating	Uńit
Vin		Vin	Vss – 0.3 to Vss + 6.0	V
		Von/off	Vss – 0.3 to V <sub>IN</sub> + 0.3	V
		Vssc	Vss – 0.3 to V <sub>IN</sub> + 0.3	V
Output voltage		V <sub>OUT</sub>	Vss – 0.3 to V <sub>IN</sub> + 0.3	V
	SOT-89-5		1000*1	mW
Power dissipation	SOT-23-5	P <sub>D</sub>	600 <sup>*1</sup>	mW
	SNT-6A(H)		500*1	mW
Operation ambient	temperature	T <sub>opr</sub>	-40 to +85	°C
Storage temperatur	e	T <sub>stg</sub>	-40 to +125	°C

<sup>\*1.</sup> When mounted on board

[Mounted board]

(1) Board size :  $114.3 \text{ mm} \times 76.2 \text{ mm} \times t1.6 \text{ mm}$ (2) Name : JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

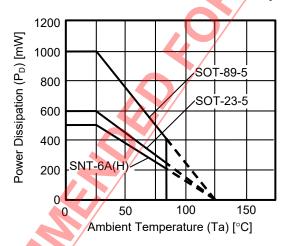


Figure 8 Power Dissipation of Package

# **■** Electrical Characteristics

# Table 10 (1 / 2)

(Ta = 25°C unless other	nuino on	√oifiod\
U a = 70 C uniess one	IVVISE SL	recineur

ltem	Symbol	C	Condition	Min.	Typ.	Max.	Unit	Test Circuit
Output voltage*1	Voure	$V_{IN} = V_{OUT(S)} + 1.0 V,$	1.2 V ≤ V <sub>OUT(S)</sub> < 1.5 V	V <sub>OUT(S)</sub> - 0.015	V <sub>OUT(S)</sub>	V <sub>OUT(s)</sub> + 0.015	V	1
Output voltage	V <sub>OUT(E)</sub>	I <sub>OUT</sub> = 100 mA	$1.5~V \leq V_{OUT(S)} \leq 3.5~V$	V <sub>OUT(S)</sub> × 0.99	V <sub>OUT(S)</sub>	V <sub>OUT(S)</sub> × 1.01	V	1
Output current*2	l <sub>out</sub>	$V_{IN} \ge V_{OUT(S)} + 1.0 \text{ V}$		300*5			mA	3
			$1.2 \text{ V} \le V_{OUT(S)} < 1.3 \text{ V}$	0.50	0.58	0.67	V	1
			$1.3 \text{ V} \le V_{OUT(S)} < 1.4 \text{ V}$	-	0.48	0.56	V	1
			$1.4 \text{ V} \le V_{OUT(S)} < 1.5 \text{ V}$		0.37	0.50	V	1
Dropout voltage*3	$V_{drop}$	I <sub>ОИТ</sub> = 300 mA	$1.5 \text{ V} \le V_{OUT(S)} < 1.7 \text{ V}$	(A-p)	0.31	0.47	V	1
Dropout voltage	v drop	1001 - 300 111A	$1.7 \text{ V} \le V_{OUT(S)} < 2.0 \text{ V}$	<b>Y</b> /	0.28	0.42	V	1
			$2.0 \text{ V} \le V_{OUT(S)} \le 2.4 \text{ V}$		0.25	0.38	V	1
			$2.4 \text{ V} \le V_{OUT(S)} \le 2.8 \text{ V}$	/_	0.23	0.35	V	1
			$2.8 \text{ V} \le V_{OUT(S)} \le 3.5 \text{ V}$	_	0.21	0.31	V	1
Line regulation	$\frac{\Delta V_{\text{OUT1}}}{\Delta V_{\text{IN}} \bullet V_{\text{OUT}}}$	V <sub>OUT(S)</sub> + 0.5 V ≤ V <sub>IN</sub> ≤ 5.5 V, l <sub>OUT</sub> = 100 mA		_	0.05	0.2	%/V	1
Load regulation	$\Delta V_{OUT2}$	$V_{IN} = V_{OUT(S)} + 1.0 \text{ V}, 1.0 \text{ mA} \le I_{OUT} \le 100 \text{ mA}$		_	15	40	mV	1
Load regulation	ΔV0012	$V_{IN} = V_{OUT(S)} + 1.0 V, 1.0$	) mA ≤ l <sub>оит</sub> ≤ 300 mA	_	45	120	mV	1
Output voltage	$\Delta V$ оυт	$V_{IN} = V_{OUT(S)} + 1.0 \text{ V}, I_{OU}$	<sub>IT</sub> = 30 mA,	_	±130	_	ppm/°C	1
temperature coefficient*4	∆Ta∙Vouτ	–40°C ≤ Ta ≤ +85°C			±130		ррпі, С	
Current consumption during operation	I <sub>SS1</sub>	$V_{IN} = V_{OUT(S)} + 1.0 \text{ V, ON}$	V / OFF pin = ON, no load	_	45	65	μΑ	2
Current consumption during power-off	I <sub>SS2</sub>	$V_{IN} = V_{OUT(S)} + 1.0 V, ON$	V / OFF pin = OFF, no load	_	0.1	1.0	μΑ	2
Input voltage	V <sub>IN</sub>		/_	1.7	_	5.5	V	_
ON / OFF pin input voltage "H"	V <sub>SH</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, R <sub>L</sub> determined by V <sub>OUT</sub> out		1.0	_	_	٧	4
ON / OFF pin input voltage "L"	V <sub>SL</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, R <sub>L</sub> determined by V <sub>OUT</sub> out	= 1.0 kΩ	_	_	0.3	V	4
ON / OFF pin		$V_{IN} = 5.5 \text{ V}$	B / D type	-0.1	_	0.1	μA	4
input current "H"	I <sub>SH</sub>	V <sub>ON/OFF</sub> = 5.5 V	A / C type	1.0	2.5	5.0	μА	4
ON / OFF pin input current "L"	I <sub>SL</sub>	V <sub>IN</sub> = 5.5 V, V <sub>ON/OFF</sub> = 0 V		-0.1	_	0.1	μА	4
Ripple rejection	RR	$V_{\text{IN}} = V_{\text{OUT}(S)} + 1.0 \text{ V, f} = 1.0 \text{ kHz,}$ $\Delta V_{\text{rip}} = 0.5 \text{ Vrms, } I_{\text{OUT}} = 50 \text{ mA}$			70		dB	5
Short-circuit current	Ishort	$V_{IN} = V_{OUT(S)} + 1.0 \text{ V, ON}$	N / OFF pin = ON, V <sub>OUT</sub> = 0 V	_	150	_	mA	3
Soft-start time	tss	` '	<sub>TT</sub> = 100 mA, C <sub>L</sub> = 1.0 μF,	_	0.7	_	ms	1

#### Table 10 (2 / 2)

S-1137 Series A / B type (With discharge shunt function)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit/	Test Circuit
Discharge shunt resistance during power-off	R <sub>LOW</sub>	V <sub>OUT</sub> = 0.1 V, V <sub>IN</sub> = 5.5 V		35		Ω	3

S-1137 Series A / C type (With pull-down resistor)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Test Circuit
Power-off pull-down resistor	R <sub>PD</sub>	_	1.0	2.2	5.0	МΩ	4

<sup>\*1.</sup> V<sub>OUT(S)</sub>: Set output voltage

V<sub>OUT(E)</sub>: Actual output voltage

Output voltage when fixing IouT (= 100 mA) and inputting VouT(S) +1.0 V

- \*2. The output current at which the output voltage becomes 95% of V<sub>OUT(E)</sub> after gradually increasing the output current.
- \*3.  $V_{drop} = V_{IN1} (V_{OUT3} \times 0.98)$

 $V_{OUT3}$  is the output voltage when  $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$  and  $I_{OUT} = 300 \text{ mA}$ .

V<sub>IN1</sub> is the input voltage at which the output voltage becomes 98% of V<sub>OUT3</sub> after gradually decreasing the input voltage.

\*4. A change in temperature of the output voltage [mV/°C] is calculated using the following equation.

$$\frac{\Delta V_{\text{OUT}}}{\Delta \text{Ta}} \text{ [mV/°C]}^{*1} = V_{\text{OUT(S)}} \text{ [V]}^{*2} \times \frac{\Delta V_{\text{OUT}}}{\Delta \text{Ta} \bullet V_{\text{OUT}}} \text{ [ppm/°C]}^{*3} \neq 1000$$

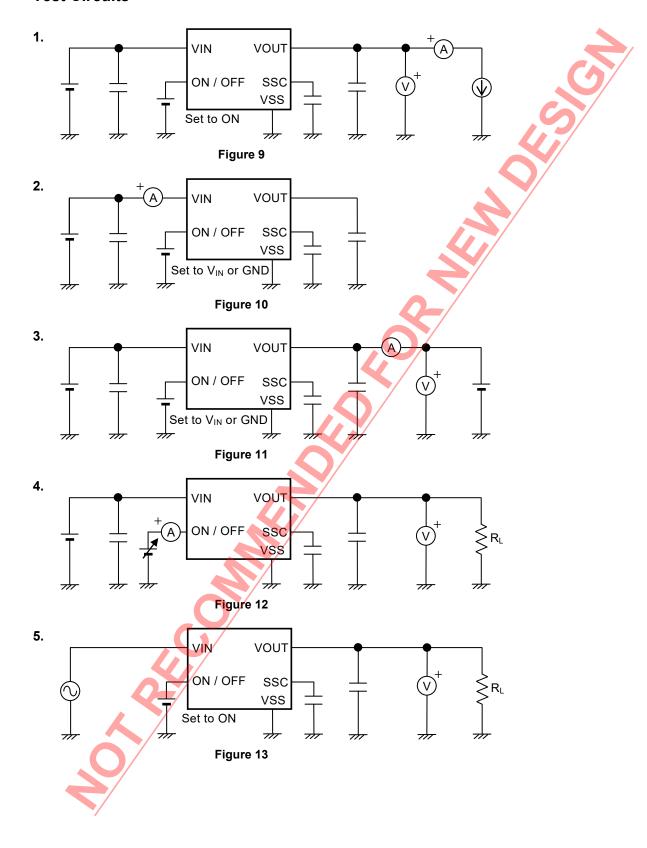
- \*1. Change in temperature of output voltage
- \*2. Set output voltage
- \*3. Output voltage temperature coefficient
- \*5. The output current can be at least this value.

Due to restrictions on the package power dissipation, this value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large.

This specification is guaranteed by design.

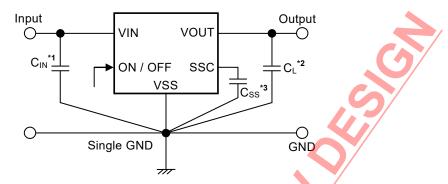


# **■** Test Circuits



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#### ■ Standard Circuit



- \*1. C<sub>IN</sub> is a capacitor for stabilizing the input.
- \*2. A ceramic capacitor of 1.0 μF or more can be used as C<sub>L</sub>.
- \*3. A ceramic capacitor of 0.22 nF to 22 nF can be used for Css.

#### Figure 14

Caution The above connection diagram and constants will not guarantee successful operation. Perform thorough evaluation using the actual application to set the constants.

# **■** Condition of Application

 $\begin{array}{ll} \mbox{Input capacitor } (C_{\mbox{\scriptsize IN}}): & 1.0 \ \mu\mbox{F or more} \\ \mbox{Output capacitor } (C_{\mbox{\scriptsize L}}): & 1.0 \ \mu\mbox{F or more} \\ \mbox{Capacitor for soft-start } (C_{\mbox{\scriptsize SS}}): & 0.22 \ n\mbox{F to } 22 \ n\mbox{F} \end{array}$ 

Caution Generally a series regulator may cause oscillation, depending on the selection of external parts.

Confirm that no oscillation occurs in the application for which the above capacitors are used.

# ■ Selection of Input and Output Capacitors (C<sub>IN</sub>, C<sub>L</sub>)

The S-1137 Series requires an output capacitor between the VOUT pin and VSS pin for phase compensation. Operation is stabilized by a ceramic capacitor with an output capacitance of 1.0  $\mu F$  or more over the entire temperature range. When using an OS capacitor, a tantalum capacitor, or an aluminum electrolytic capacitor, the capacitance must be 1.0  $\mu F$  or more.

The value of the output overshoot or undershoot transient response varies depending on the value of the output capacitor. The required capacitance of the input capacitor differs depending on the application.

The recommended capacitance for an application is  $C_{IN} \ge 1.0~\mu F$ ,  $C_L \ge 1.0~\mu F$ ; however, when selecting the output capacitor, perform sufficient evaluation including evaluation of temperature characteristics with the actual device.

# ■ Selection of Capacitor for Soft-start (Css)

The S-1137 Series requires a capacitor for soft-start ( $C_{SS}$ ) between the SSC pin and VSS pin. Over the entire temperature range, the S-1137 Series operates stably with a ceramic capacitor of 0.22 nF or more. According to  $C_{SS}$  capacitance, the rising speed of the output voltage is adjustable. The time that the output voltage rises to 99% is 0.7 ms typ. at  $C_{SS}$  = 1.0 nF. The recommended value for applications is 0.22 nF  $\leq C_{SS} \leq$  22 nF, however, define the values by sufficient evaluation including the temperature characteristics under the usage condition.

**S-1137 Series** Rev.2.3\_00

# **■** Explanation of Terms

#### 1. Low dropout voltage regulator

This voltage regulator has the low dropout voltage due to its built-in low on-resistance transistor.

# 2. Output voltage (Vout)

The accuracy of the output voltage is ensured at  $\pm 1.0\%$  or  $\pm 15$  mV\*1 under the specified conditions of fixed input voltage\*2, fixed output current, and fixed temperature.

- \*1. When  $V_{OUT} < 1.5 \text{ V} : \pm 15 \text{ mV}$ , When  $1.5 \text{ V} \le V_{OUT} : \pm 1.0\%$
- \*2. Differs depending on the product.

Caution If the above conditions change, the output voltage value may vary and exceed the accuracy range of the output voltage. Refer to "■ Electrical Characteristics" and "■ Characteristics (Typical Data)" for details.

3. Line regulation 
$$\left(\frac{\Delta V_{\text{OUT1}}}{\Delta V_{\text{IN}} \bullet V_{\text{OUT}}}\right)$$

Indicates the dependency of the output voltage on the input voltage. That is, the values show how much the output voltage changes due to a change in the input voltage with the output current remaining unchanged.

# 4. Load regulation (ΔV<sub>OUT2</sub>)

Indicates the dependency of the output voltage on the output current. That is, the values show how much the output voltage changes due to a change in the output current with the input voltage remaining unchanged.

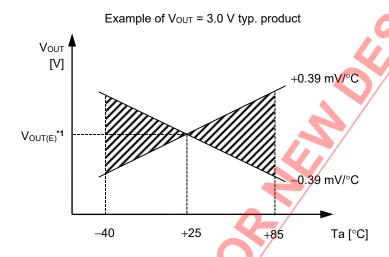
# 5. Dropout voltage (V<sub>drop</sub>)

Indicates the difference between input voltage ( $V_{IN1}$ ) and the output voltage when; decreasing input voltage ( $V_{IN}$ ) gradually until the output voltage has dropped out to the value of 98% of output voltage ( $V_{OUT3}$ ), which is at  $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$ .

$$V_{drop} = V_{IN1} - (V_{OUT3} \times 0.98)$$

# 6. Output voltage temperature coefficient $\left(\frac{\Delta V_{\text{OUT}}}{\Delta \text{Ta} \bullet V_{\text{OUT}}}\right)$

The shaded area in **Figure 15** is the range where  $V_{OUT}$  varies in the operation temperature range when the output voltage temperature coefficient is  $\pm 130$  ppm/°C.



\*1.  $V_{OUT(E)}$  is the value of the output voltage measured at Ta = +25°C.

Figure 15

A change in the temperature of the output voltage [mV/°C] is calculated using the following equation.

$$\frac{\Delta V_{\text{OUT}}}{\Delta \text{Ta}} \left[ \text{mV/}^{\circ} \text{C} \right]^{*1} = V_{\text{OUT}(S)} \left[ \text{V} \right]^{*2} \times \frac{\Delta V_{\text{OUT}}}{\Delta \text{Ta} \bullet V_{\text{OUT}}} \left[ \text{ppm/}^{\circ} \text{C} \right]^{*3} \div 1000$$

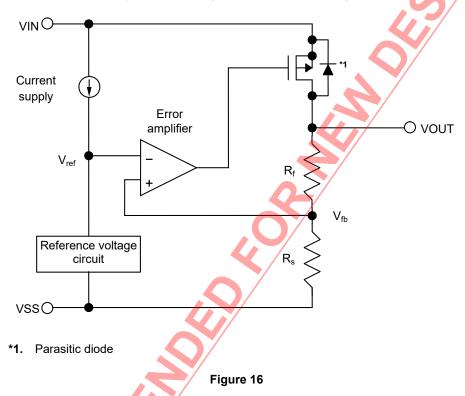
- \*1. Change in temperature of output voltage
- \*2. Set output voltage
- \*3. Output voltage temperature coefficient

# Operation

#### 1. Basic operation

Figure 16 shows the block diagram of the S-1137 Series.

The error amplifier compares the reference voltage ( $V_{ref}$ ) with feedback voltage ( $V_{fb}$ ), which is the output voltage resistance-divided by feedback resistors ( $R_s$  and  $R_f$ ). It supplies the gate voltage necessary to maintain the constant output voltage which is not influenced by the input voltage and temperature change, to the output transistor.



# 2. Output transistor

In the S-1137 Series, a low on-resistance P-channel MOS FET is used as the output transistor.

Be sure that  $V_{\text{OUT}}$  does not exceed  $V_{\text{IN}} + 0.3$  V to prevent the voltage regulator from being damaged due to reverse current flowing from the VOUT pin through a parasitic diode to the VIN pin, when the potential of  $V_{\text{OUT}}$  became higher than  $V_{\text{IN}}$ .

# 3. ON / OFF pin

This pin starts and stops the regulator.

When the ON / OFF pin is set to OFF level, the entire internal circuit stops operating, and the built-in P-channel MOS FET output transistor between the VIN pin and VOUT pin is turned off, reducing current consumption significantly. The S-1137 Series has a discharge shunt circuit which discharges the capacitor for soft-start ( $C_{SS}$ ) so that the SSC pin is forcibly set to the  $V_{SS}$  level.

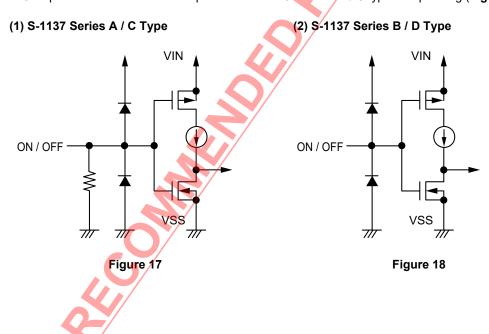
Since the S-1137 Series A / B types have a built-in discharge shunt circuit to discharge the output capacitance, the VOUT pin is forcibly set to  $V_{SS}$  level. In the S-1137 Series C / D types, the VOUT pin is set to  $V_{SS}$  level through several hundred  $k\Omega$  internal divided resistors between the VOUT pin and VSS pin.

Note that the current consumption increases when a voltage of 0.3 V to 1.0 V (Ta =  $25^{\circ}$ C) is applied to the ON / OFF pin. The ON / OFF pin is configured as shown in **Figures 17** and **18**. In the S-1137 Series A / C types, the ON / OFF pin is internally pulled down to the VSS pin in the floating status, so the VOUT pin is set to the V<sub>SS</sub> level. In the S-1137 Series B / D types, the ON / OFF pin is not internally pulled down, so do not use the pin in the floating status. When not using the ON / OFF pin in the S-1137 Series B / D types, connect the pin to the VIN pin.

Table 11

Product Type	ON / OFF Pin	Internal Circuit	VOUT Pin Voltage	Current Consumption
A/B/C/D	"H" : ON	Operate	Set value	Iss1*1
A/B/C/D	"L" : OFF	Stop	Vss level	Iss <sub>2</sub>

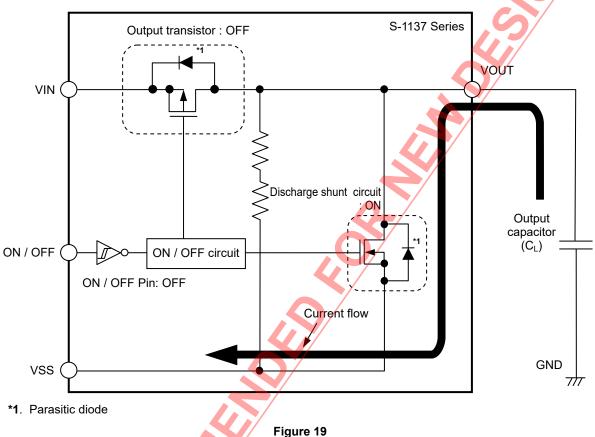
<sup>\*1.</sup> IC's current consumption increases as much as current flows into the pull-down resistor when the ON / OFF pin is connected to the VIN pin and the S-1137 Series A / C types is operating (Figure 17).



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#### 4. Discharge shunt function (S-1137 Series A / B types)

The S-1137 Series A / B types have a built-in discharge shunt circuit to discharge the output capacitance. When the ON / OFF pin is set to OFF level, turns the output transistor off, and turns the discharge shunt circuit on so that the output capacitor discharges. These types allow for the VOUT pin reach the V<sub>SS</sub> level faster than the S-1137 Series C / D types that does not have a discharge shunt circuit.



#### 5. Overcurrent protection circuit

The S-1137 Series has an overcurrent protection circuit having the characteristics shown in "(1) Output Voltage vs. Output Current (When Load Current Increases) (Ta = 25°C)" in "
Characteristics (Typical Data)", in order to protect the output transistor against an excessive output current and short circuiting between the VOUT pin and VSS pin. The current when the output pin is short-circuited (Ishort) is internally set at approx. 150 mA typ., and the normal value is restored for the output voltage, if releasing a short circuit once.

Caution This overcurrent protection circuit does not work as for thermal protection. If this IC long keeps short circuiting inside, pay attention to the conditions of input voltage and load current so that, under the usage conditions including short circuit, the loss of the IC will not exceed power dissipation of the package.

#### 6. Pull-down resistor (S-1137 Series A / C types)

In the S-1137 Series A / C types, the ON / OFF pin is internally pulled down to the VSS pin in the floating status, so the VOUT pin is set to the Vss level.

IC's current consumption increases as much as current flows into the pull-down resistor of 2.2 M $\Omega$  typ. when the ON / OFF pin is connected to the VIN pin and the S-1137 Series A / C types is operating.

#### 7. Soft-start function

The S-1137 Series has a soft-start circuit to suppress the rush current (I<sub>RUSH</sub>) and overshoot of the output voltage generated at power-on or at the time when the ON / OFF pin is set to ON.

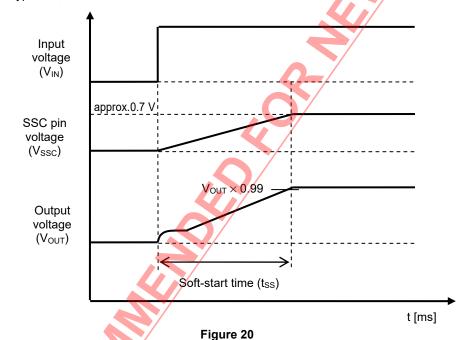
Immediately after power-on or after the ON / OFF pin is set to ON, the output voltage slowly rises. The time that the output voltage rises to 99% ( $t_{SS}$ ) is adjustable by a capacitor ( $t_{SS}$ ) externally connected between the SSC pin and VSS pin. When  $t_{SS}$  = 1.0 nF, the time is 0.7 ms typ. At the time when the ON / OFF pin is set to OFF, the electric charge charged in the external capacitor ( $t_{SS}$ ) will be discharged by the discharge shunt transistor.

The soft-start time ( $t_{SS}$ ) is defined by time consonants of the built-in constant current (approx. 1  $\mu$ A) and the external capacitor ( $C_{SS}$ ):

 $t_{SS}$  (ms) = soft-start coefficient ×  $C_{SS}$  (nF)

The soft-start coefficient (25°C) is as follows.

Min.: 0.4, Typ.: 0.7, Max.: 1.2



The rush current can be suppressed greatly by the soft-start function.

As the value of the external capacitor is larger, the rush current decreases more.

Figure 21 shows the waveform example of the rush current.

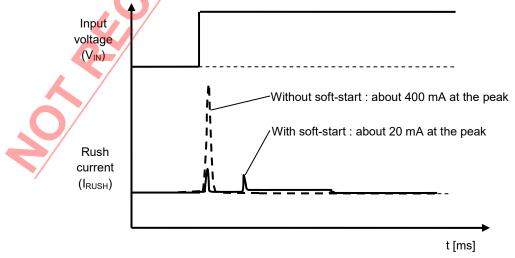


Figure 21 Waveform Example of Rush Current

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#### ■ Precautions

- Wiring patterns for the VIN pin, the VOUT pin and GND should be designed so that the impedance is low. When mounting an output capacitor between the VOUT pin and VSS pin (C<sub>L</sub>), the capacitor for stabilizing the input between the VIN pin and VSS pin (C<sub>IN</sub>), and the capacitor for soft-start between the SSC pin and VSS pin, the distance from the capacitors to these pins should be as short as possible.
- Note that generally the output voltage may increase when a series regulator is used at low load current (1.0 mA or less).
- Note that generally the output voltage may increase due to the leakage current from an output driver when a series regulator is used at high temperature.
- Generally a series regulator may cause oscillation, depending on the selection of external parts. The following conditions are recommended for the S-1137 Series. However, be sure to perform sufficient evaluation under the actual usage conditions for selection, including evaluation of temperature characteristics. Refer to "(8) Example of Equivalent Series Resistance vs. Output Current Characteristics (Ta = 25°C)" in "■ Reference Data" for the equivalent series resistance (R<sub>ESR</sub>) of the output capacitor.

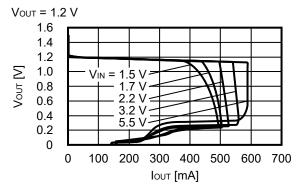
 $\begin{array}{ll} \text{Input capacitor } (C_{\text{IN}}): & 1.0 \ \mu\text{F or more} \\ \text{Output capacitor } (C_{\text{L}}): & 1.0 \ \mu\text{F or more} \\ \text{Capacitor for soft-start } (C_{\text{SS}}): & 0.22 \ \text{nF to } 22 \ \text{nF} \end{array}$ 

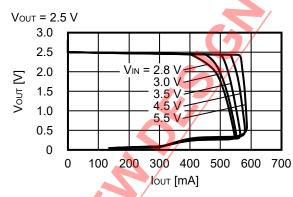
- The voltage regulator may oscillate when the impedance of the power supply is high and the input capacitance is small or an input capacitor is not connected.
- If the output capacitance is small, power supply's fluctuation and the characteristics of load fluctuation become worse. Sufficiently evaluate the output voltage's fluctuation with the actual device.
- Although the S-1137 Series has a soft-start circuit, overshoot may occur in the output voltage momentarily when the capacitance of the capacitor for soft-start (Css) is small. Perform sufficient evaluation with actual devices.
- The application conditions for the input voltage, output voltage, and load current should not exceed the package power dissipation.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- In determining the output current, attention should be paid to the output current value specified in **Table 10** in "■ **Electrical Characteristics**" and footnote \*5 of the table.
- ABLIC Inc. claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.

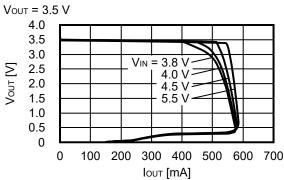


# ■ Characteristics (Typical Data)

#### (1) Output Voltage vs. Output Current (When Load Current Increases) (Ta = 25°C)



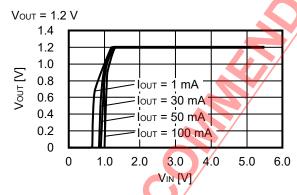


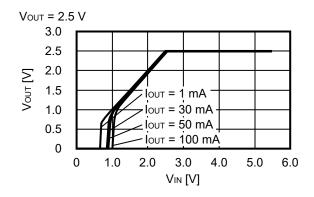


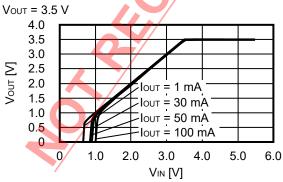
**Remark** In determining the output current, attention should be paid to the following.

- 1. The minimum output current value and footnote \*5 in Table 10 in the "■ Electrical Characteristics"
- 2. The package power dissipation

(2) Output Voltage vs. Input Voltage (Ta = 25°C)

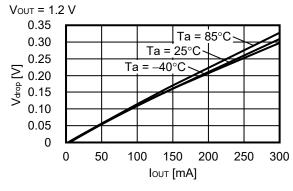


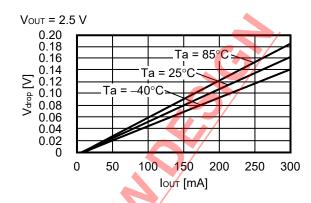


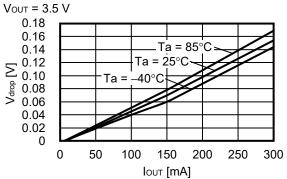


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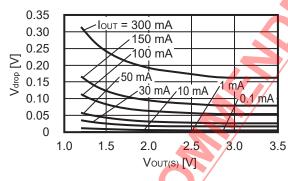
#### (3) Dropout Voltage vs. Output Current



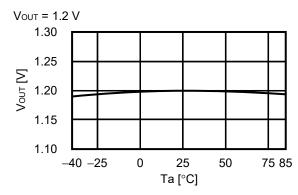


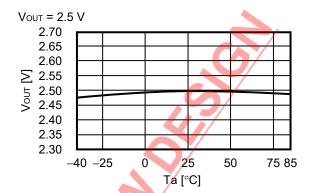


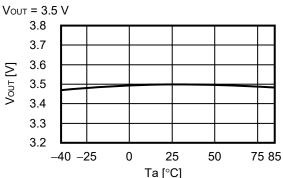
# (4) Dropout Voltage vs. Set Output Voltage

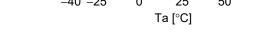


# (5) Output Voltage vs. Ambient Temperature

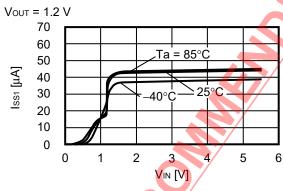


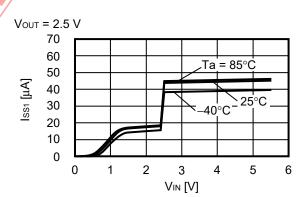


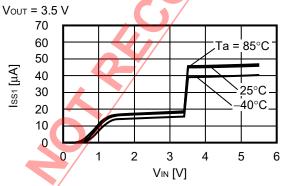




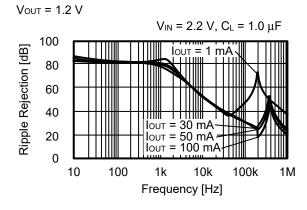
# (6) Current Consumption vs. Input Voltage

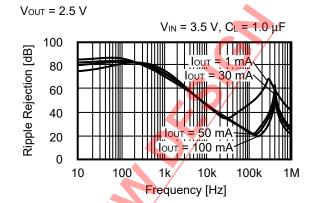


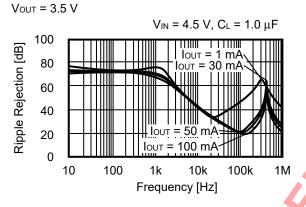




# (7) Ripple Rejection (Ta = 25°C)

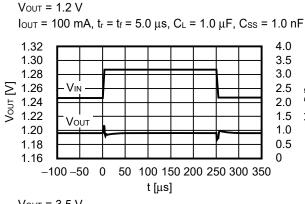






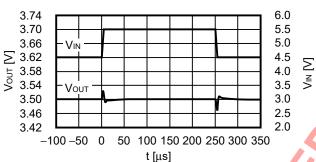
#### ■ Reference Data

#### (1) Transient Response Characteristics when Input (Ta = 25°C)



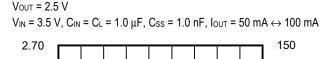
Vout = 2.5 V  $I_{OUT} = 100 \text{ mA}, t_r = t_f = 5.0 \mu\text{s}, C_L = 1.0 \mu\text{F}, C_{SS} = 1.0 \text{ nF}$ 5.0 2.74 2.70 4.5 2.66 Vin 4.0 ≥ 2.62 3.5 5 2.58 2.54 2.58 3.0 2.5 Vout 2.50 2.0 2.46 1.5 2.42 1.0 50 100 150 200 250 300 350 -100 -50 0t [μs]

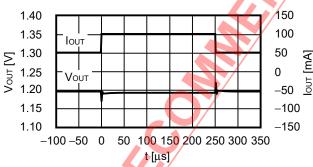
 $V_{OUT} = 3.5 \ V$   $I_{OUT} = 100 \ mA, \ t_r = t_f = 5.0 \ \mu s, \ C_L = 1.0 \ \mu F, \ C_{SS} = 1.0 \ nF$ 

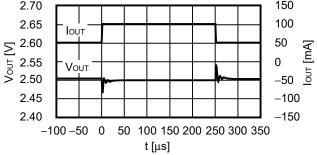


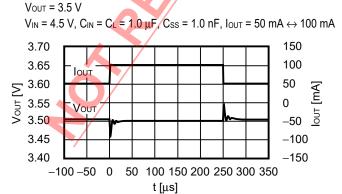
# (2) Transient Response Characteristics of Load (Ta = 25°C)

Vout = 1.2 V  $V_{IN} = 2.2 \text{ V}, C_{IN} = C_L = 1.0 \text{ }\mu\text{F}, C_{SS} = 1.0 \text{ }n\text{F}, lout} = 50 \text{ }m\text{A} \leftrightarrow 100 \text{ }m\text{A}$ 1.40





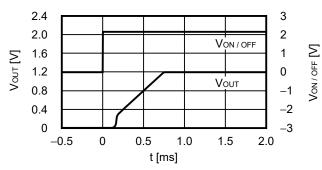


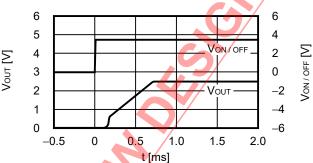


# (3) Transient Response Characteristics of ON / OFF Pin (Ta = 25°C)

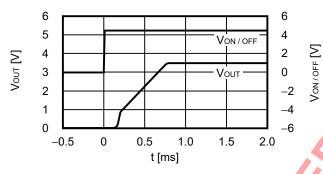
 $V_{\text{IN}} = 2.2 \text{ V}$ ,  $t_r = 1.0 \ \mu\text{s}$ ,  $C_{\text{IN}} = C_L = 1.0 \ \mu\text{F}$ ,  $C_{\text{SS}} = 1.0 \ \text{nF}$ ,  $I_{\text{OUT}} = 100 \ \text{mA}$ 

Vout = 2.5 V V<sub>IN</sub> = 3.5 V, t<sub>r</sub> = 1.0  $\mu$ s, C<sub>IN</sub> = C<sub>L</sub> = 1.0  $\mu$ F, C<sub>SS</sub> = 1.0 nF, lou<sub>V</sub> = 100 mA



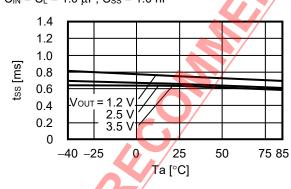


Vout = 3.5 V  $V_{IN} = 4.5 \text{ V, } t_r = 1.0 \text{ } \mu\text{s, } C_{IN} = C_L = 1.0 \text{ } \mu\text{F, } C_{SS} = 1.0 \text{ } n\text{F, } l_{OUT} = 100 \text{ } m\text{A}$ 

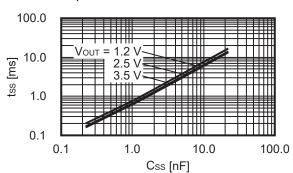


(4) Soft-start Time vs. Ambient Temperature Characteristics (5) Soft-start time vs. Capacitor for Soft-start Characteristics (Ta = 25°C)

$$\begin{split} V_{IN} &= V_{OUT} + 1.0 \text{ V}, V_{ON / OFF} = 0 \text{ V} \rightarrow V_{OUT} + 1.0 \text{ V}, \\ C_{IN} &= C_L = 1.0 \text{ } \mu\text{F}, C_{SS} = 1.0 \text{ nF} \end{split}$$

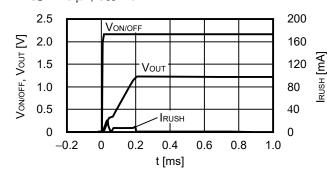


$$\begin{split} V_{IN} &= V_{OUT} + 1.0 \ V, \ V_{ON \, / \, OFF} = 0 \ V \rightarrow V_{OUT} + 1.0 \ V, \\ C_{IN} &= C_L = 1.0 \ \mu F \end{split}$$



#### (6) Rush Current Characteristics (Ta = 25°C)

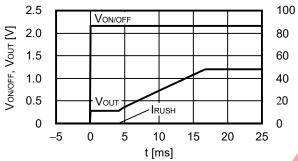
$$\begin{split} &V_{OUT} = 1.2 \ V, \ V_{IN} = 2.2 \ V, \ I_{OUT} = 0 \ mA, \\ &C_L = 1.0 \ \mu F, \ C_{SS} = 0.22 \ nF \end{split}$$



 $V_{OUT}$  = 1.2 V,  $V_{IN}$  = 2.2 V,  $I_{OUT}$  = 0 mA,  $C_L$  = 1.0  $\mu F$ ,  $C_{SS}$  = 1.0 n F

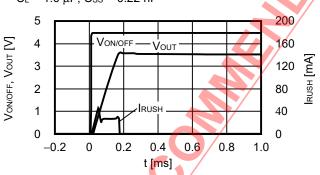


$$\begin{split} V_{\text{OUT}} = 1.2 \text{ V, } V_{\text{IN}} = 2.2 \text{ V, } I_{\text{OUT}} = 0 \text{ mA,} \\ C_{\text{L}} = 1.0 \text{ } \mu\text{F, } C_{\text{SS}} = 22.0 \text{ nF} \end{split}$$

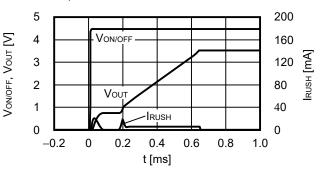


30 60 | [WA] | WA]

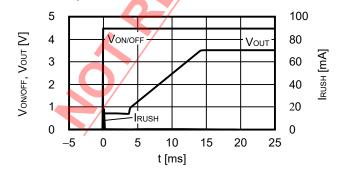
 $V_{OUT} = 3.5 \text{ V}, V_{IN} = 4.5 \text{ V}, I_{OUT} = 0 \text{ mA},$   $C_L = 1.0 \ \mu\text{F}, C_{SS} = 0.22 \text{ nF}$ 



 $V_{OUT}$  = 3.5 V,  $V_{IN}$  = 4.5 V,  $I_{OUT}$  = 0 mA,  $C_L$  = 1.0  $\mu F$ ,  $C_{SS}$  = 1.0 nF



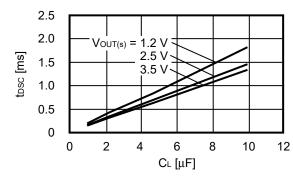
 $V_{OUT} = 3.5 \text{ V}, V_{IN} = 4.5 \text{ V}, I_{OUT} = 0 \text{ mA},$  $C_L = 1.0 \mu\text{F}, C_{SS} = 22.0 \text{ nF}$ 



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# (7) Output Capacitance vs. Characteristics of Discharge Time (Ta = 25°C) S-1137 Series A / B type (with discharge shunt function)

 $\begin{aligned} &V_{IN} = V_{OUT} + 1.0 \text{ V, } I_{OUT} = no \text{ load} \\ &V_{ON / OFF} = V_{OUT} + 1.0 \text{ V} \rightarrow V_{SS}, \, t_f = 1 \text{ } \mu s \end{aligned}$ 



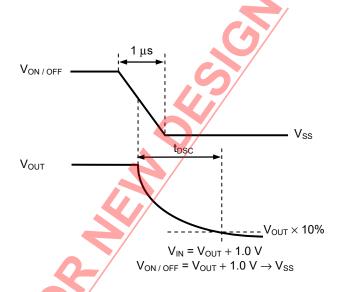
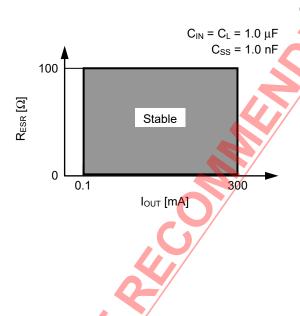
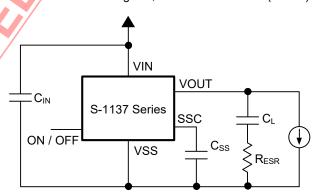


Figure 22 Measurement Condition of Discharge Time

# (8) Example of Equivalent Series Resistance vs. Output Current Characteristics (Ta = 25°C)

 $C_L$ : Murata Manufacturing Co., Ltd. GRM155B31A105K (1.0  $\mu$ F) Css: Murata Manufacturing Co., Ltd. GRM40B102K (1.0 nF)

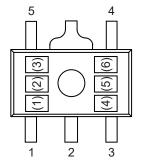




# ■ Marking Specifications

#### 1. SOT-89-5

Top view



(1) to (3) : Product code (Refer to **Product name vs. Product code**)

(4) to (6) : Lot number

#### Product name vs. Product code

#### (a) S-1137 Series A type

Product Name	Product code				
Product Name	(1)	(2)	(3)		
S-1137A12-U5T1x	V	Н	С		
S-1137A18-U5T1x	V	Н	-		
S-1137A25-U5T1x	V	Н	Q		
S-1137A33-U5T1x	V	Н	Z		

# (b) S-1137 Series B type

Disable at Name	Pr	Product code			
Product Name	(1)	(2)	(3)		
S-1137B12-U5T1x	V	I	С		
S-1137B18-U5T1x	V	I	I		
S-1137B25-U5T1x	V	I	Q		
S-1137B33-U5T1x	V	I	Z		

Remark 1. x: G or U

2. Please select products of environmental code = U for Sn 100%, halogen-free products.

# (c) S-1137 Series C type

Product Name	Pr	oduct co	de
Product Name	(1)	(2)	(3)
S-1137C12-U5T1x	V	7	С
S-1137C18-U5T1x	V	J	
S-1137C25-U5T1x	V	J	Q
S-1137C33-U5T1x	V	J	Z

# (d) S-1137 Series D type

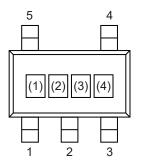
Product Name	Product code			
Product Name	(1)	(2)	(3)	
S-1137D12-U5T1x	V	K	С	
S-1137D18-U5T1x	V	K	I	
S-1137D25-U5T1x	V	K	Q	
S-1137D33-U5T1x	V	K	Z	

Remark 1. x: G or U

2. Please select products of environmental code = U for Sn 100%, halogen-free products.

# 2. SOT-23-5

Top view



(1) to (3) : Product code (Refer to **Product name vs. Product code**)

(4) : Lot number

#### Product name vs. Product code

# (a) S-1137 Series A type

Product Name	Pr	oduct co	de
Product Name	(1)	(2)	(3)
S-1137A12-M5T1y	V	Н	С
S-1137A18-M5T1y	V	Н	I
S-1137A25-M5T1y	V	Н	Q
S-1137A33-M5T1y	V	Н	Z

# (b) S-1137 Series B type

Developed Norma	Product code			
Product Name	(1)	(2)	(3)	
S-1137B12-M5T1y	V	I	С	
S-1137B18-M5T1y	V	I	I	
S-1137B25-M5T1y	V	I	Q	
\$-1137B33-M5T1y	V	I	Z	

Remark 1. y: S or U

2. Please select products of environmental code = U for Sn 100%, halogen-free products.

#### (c) S-1137 Series C type

Product Name	Product code			
Product Name	(1)	(2)	(3)	
S-1137C12-M5T1y	V		С	
S-1137C18-M5T1y	V	3		
S-1137C25-M5T1y	V	J	Q	
S-1137C33-M5T1y	V	J	Z	

(d) S-1137 Series D type

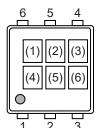
Product Name	Product code		
	(1)	(2)	(3)
S-1137D12-M5T1y	V	K	С
S-1137D18-M5T1y	V	K	I
S-1137D25-M5T1y	V	K	Q
S-1137D33-M5T1y	V	K	Z

Remark 1. y: S or U

2. Please select products of environmental code = U for Sn 100%, halogen-free products.

# 3. SNT-6A(H)

Top view



(1) to (3) : Product code (Refer to **Product name vs. Product code**)

(4) to (6) : Lot number

# Product name vs. Product code

# (a) S-1137 Series A type

Product Name	Product code		
Product Name	(1)	(2)	(3)
S-1137A12-I6T2U	V	Н	С
S-1137A18-I6T2U	V	Н	1
S-1137A25-I6T2U	V	Н	Q
S-1137A33-I6T2U	V	Η	Z

# (b) S-1137 Series B type

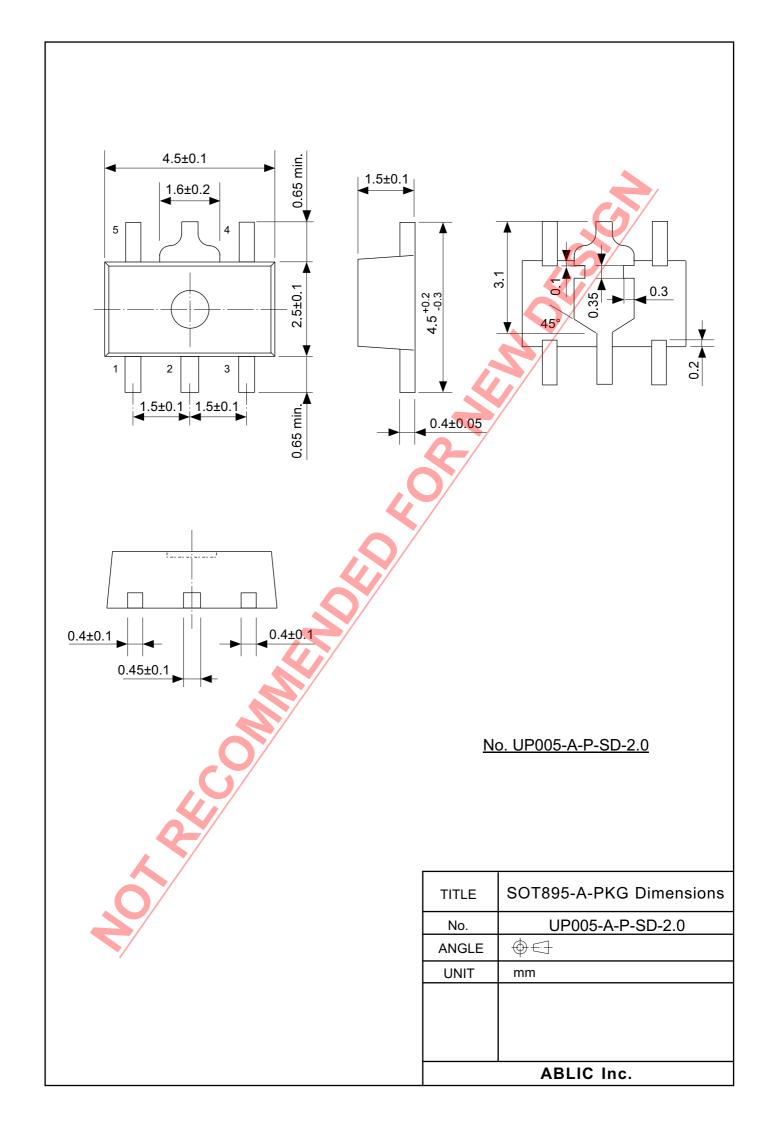
Product Name	Product code		
	(1)	(2)	(3)
S-1137B12-I6T2U	V	I	С
S-1137B18-I6T2U	V	I	I
S-1137B25-I6T2U	V	I	Q
S-1137B33-I6T2U	V	I	Z

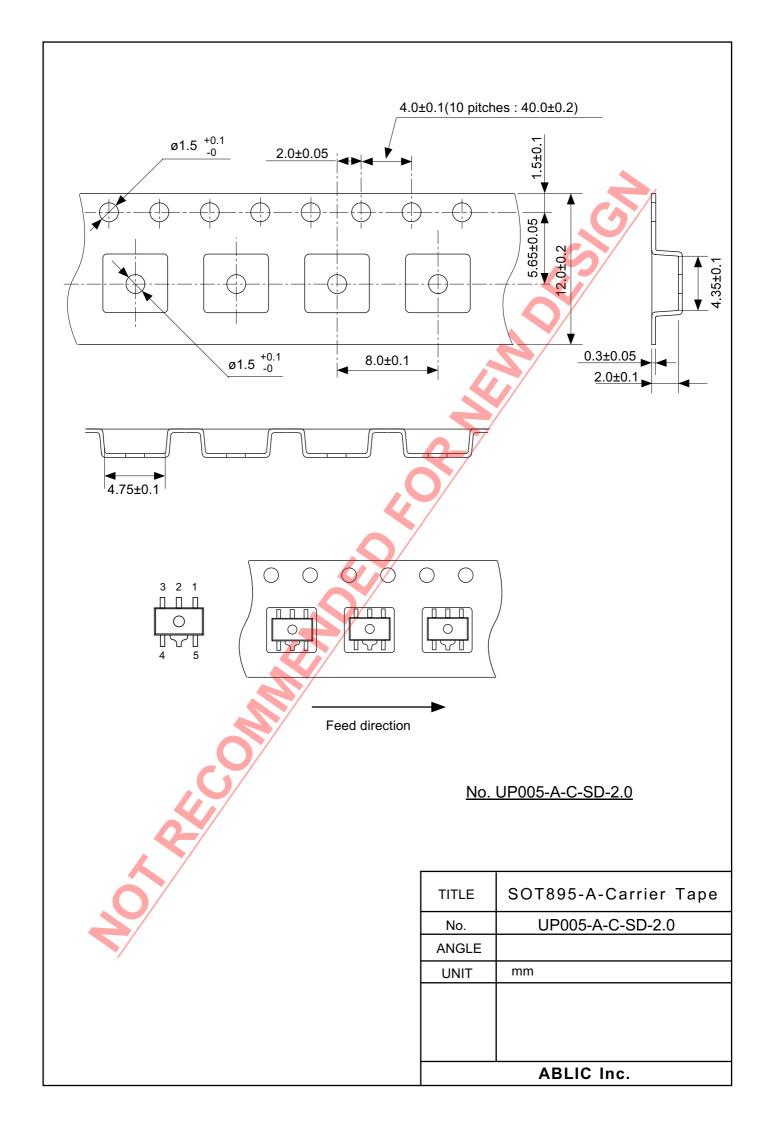
# (c) S-1137 Series C type

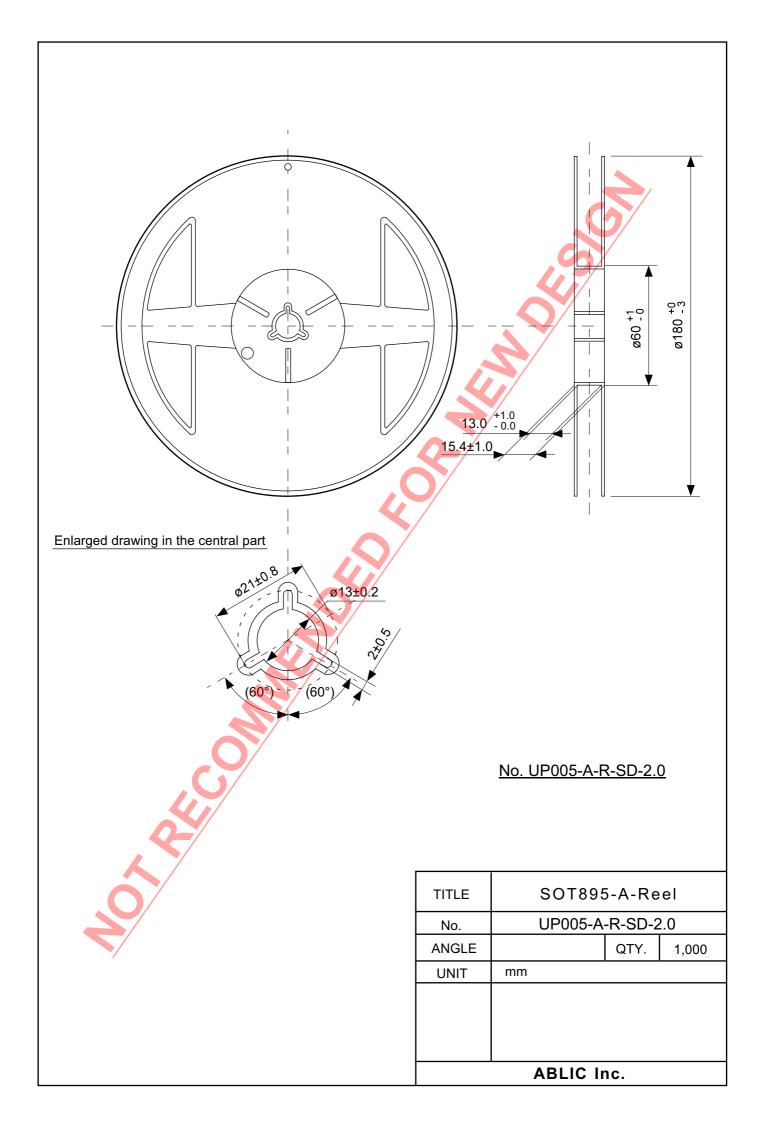
Decelerat Name	Product code		
Product Name	(1)	(2)	(3)
S-1137C12-I6T2U	V	J	C
S-1137C18-I6T2U	V	J	
S-1137C25-I6T2U	V	J	Q
S-1137C33-I6T2U	V	J	/ Z

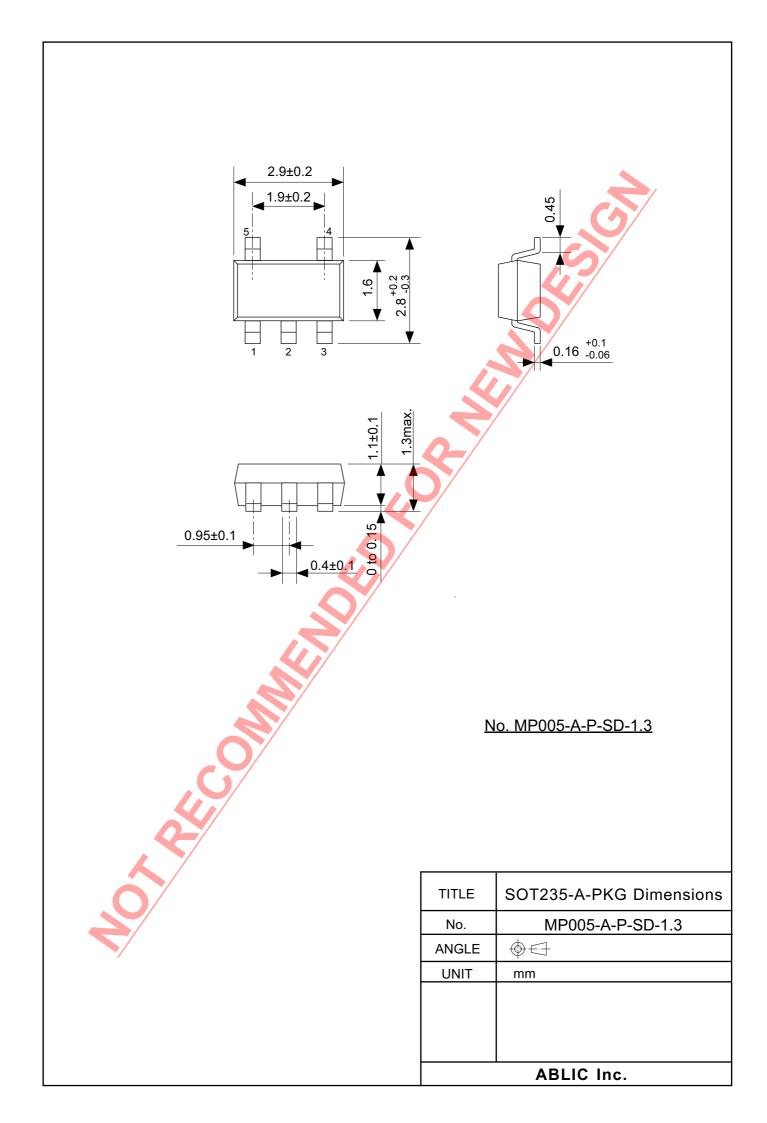
# (d) S-1137 Series D type

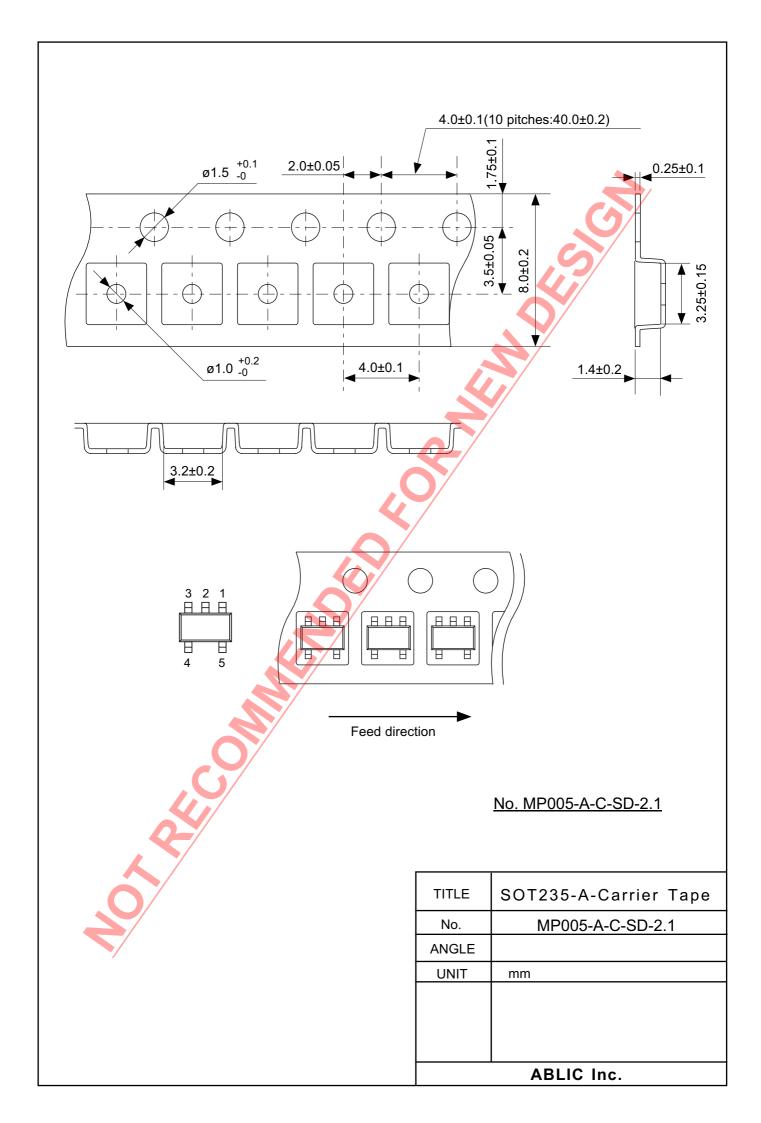
Due deset Nove	Product code		
Product Name	(1)	(2)	(3)
S-1137D12-I6T2U	V	K	С
S-1137D18-I6T2U	V	K	I
S-1137D25-I6T2U	V	K	Q
S-1137D33-I6T2U	V	K	Z

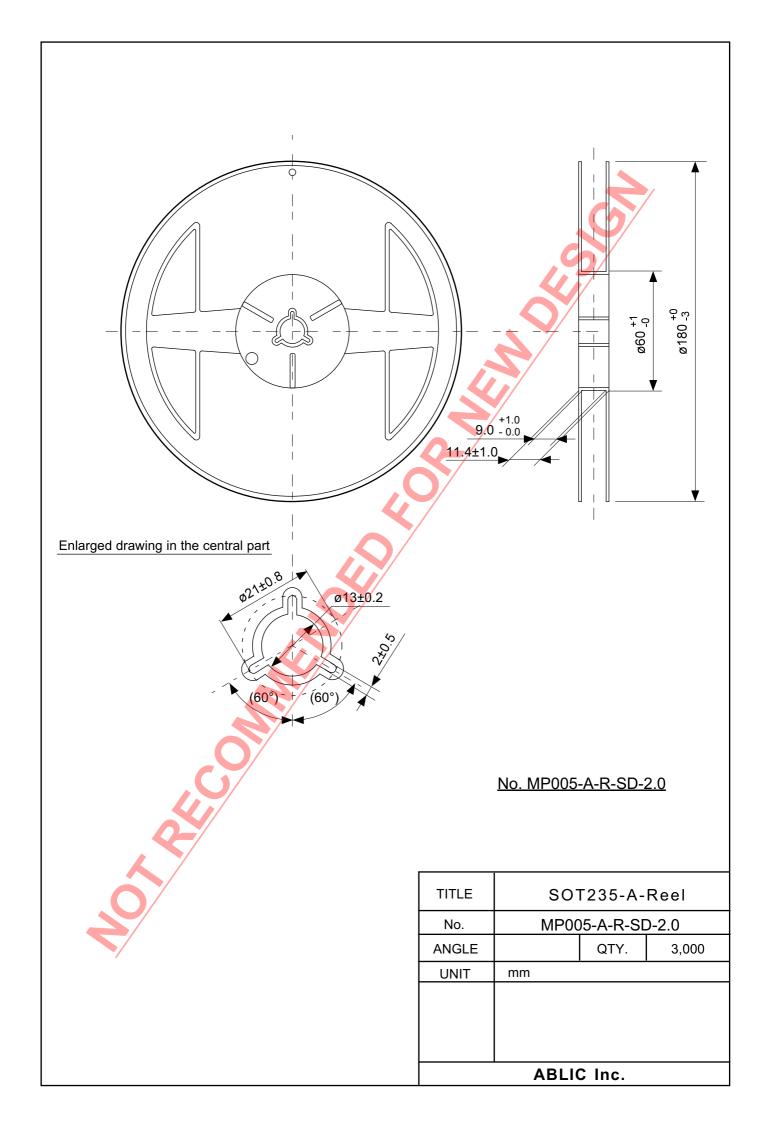


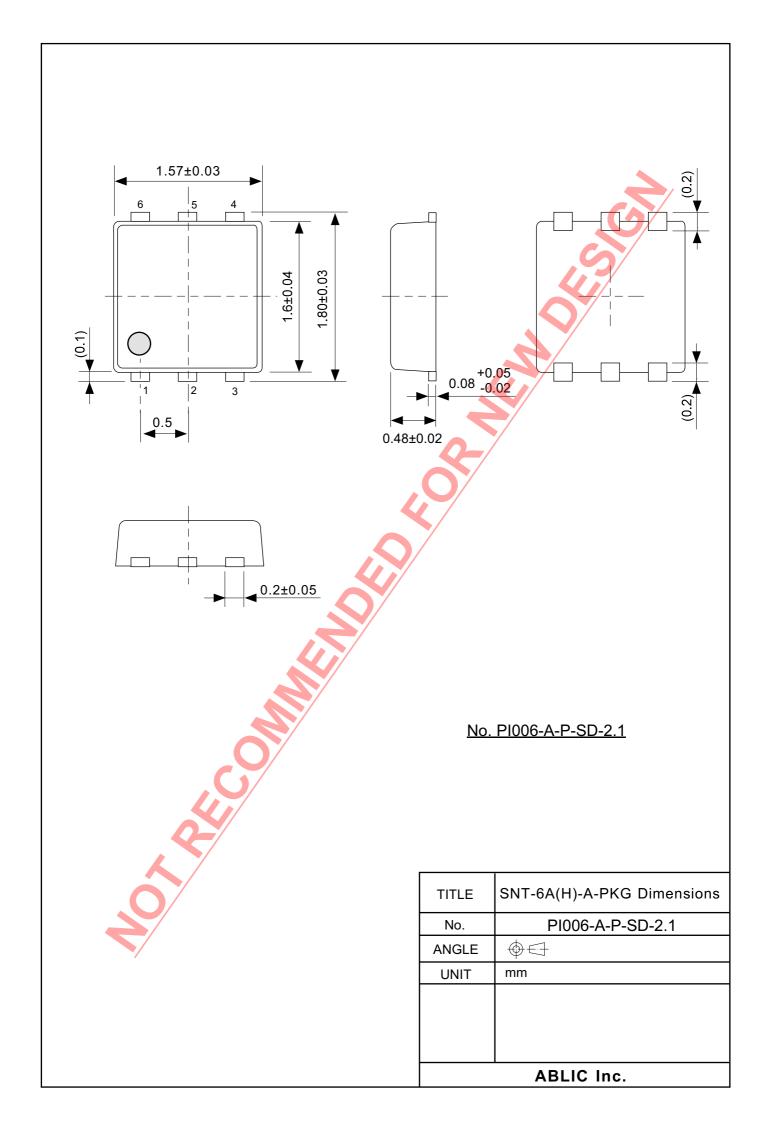


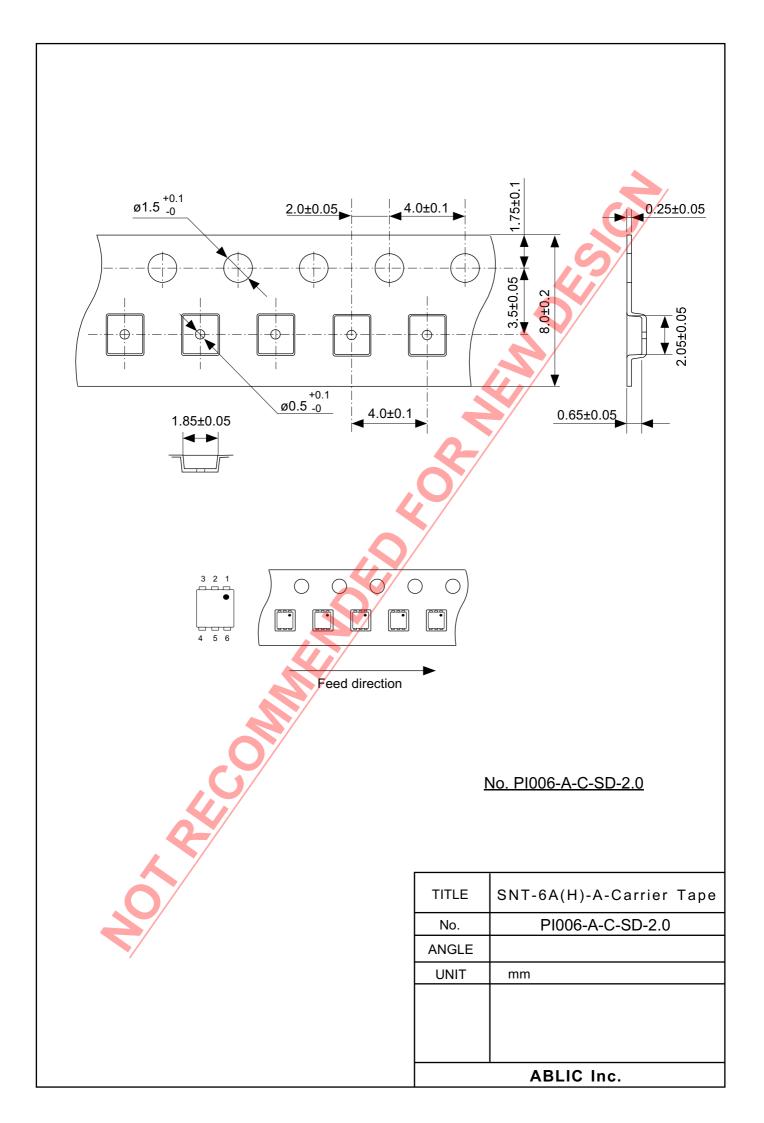


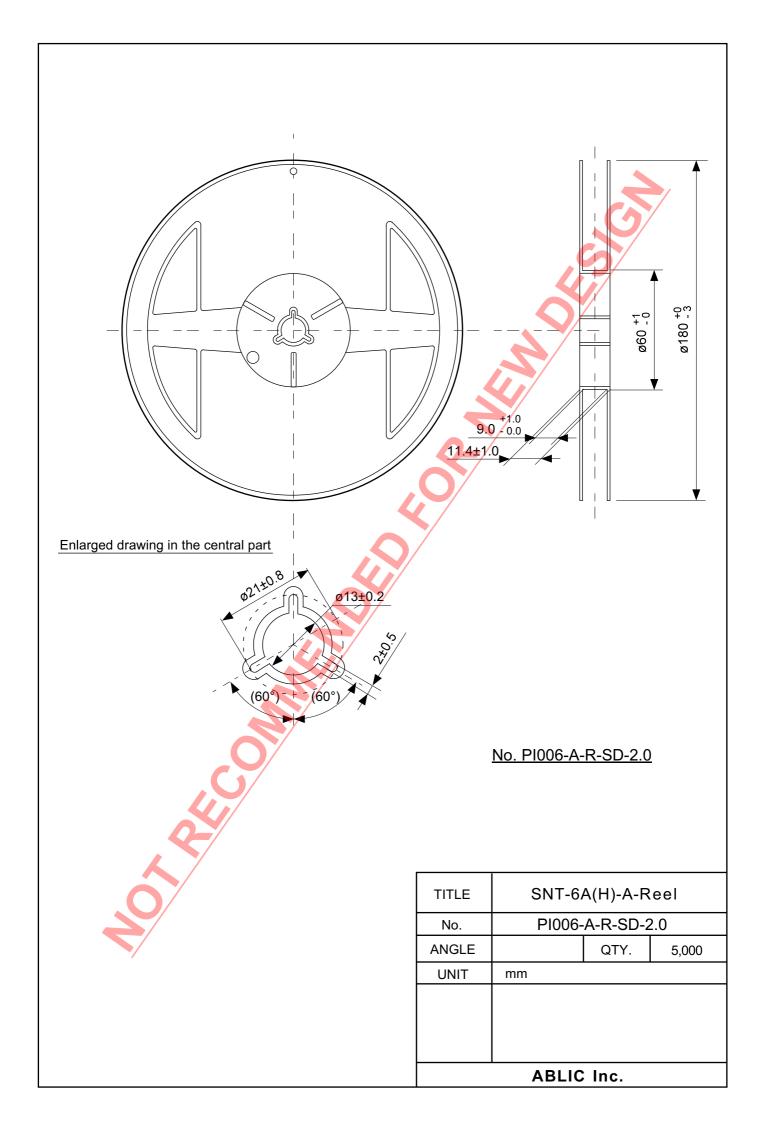


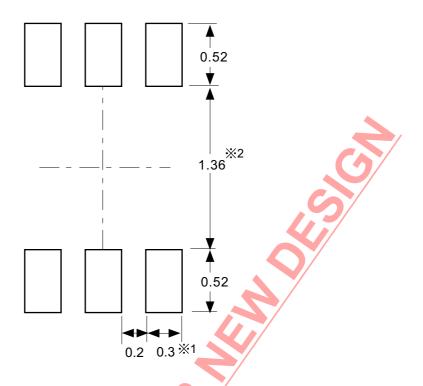












- ※1. ランドパターンの幅に注意してください (0.25 mm min. / 0.30 mm typ.)。 ※2. パッケージ中央にランドパターンを広げないでくだ<mark>さ</mark>い (1.30 mm ~ 1.40 mm)。
- 注意 1. パッケージのモールド樹脂下にシルク印刷やハンダ印刷などしないでください。
  - 2. パッケージ下の配線上のソルダーレジストなどの厚みをランドパターン表面から0.03 mm 以下にしてください。
  - 3. マスク開口サイズと開口位置はランドパターンと合わせてください。
  - 4. 詳細は "SNTパッケージ活用の手引き"を参照してください。
- \*1. Pay attention to the land pattern width (0.25 mm min. / 0.30 mm typ.).
- ※2. Do not widen the land pattern to the center of the package (1.30 mm to 1.40 mm).
- Caution 1. Do not do silkscreen printing and solder printing under the mold resin of the package.
  - 2. The thickness of the solder resist on the wire pattern under the package should be 0.03 mm or less from the land pattern surface.
  - 3. Match the mask aperture size and aperture position with the land pattern.
  - 4. Refer to "SNT Package User's Guide" for details.
- ※1. 请注意焊盘模式的宽度 (0.25 mm min. / 0.30 mm typ.)。
- ※2. 请勿向封装中间扩展焊盘模式 (1.30 mm~1.40 mm)。
- 注意 1. 请勿在树脂型封装的不面印刷丝网、焊锡。
  - 2. 在封装下、布线上的阻焊膜厚度 (从焊盘模式表面起) 请控制在 0.03 mm 以下。
  - 3. 钢网的开口尺寸和开口位置请与焊盘模式对齐。
  - 4. 详细内容请参阅 "SNT 封装的应用指南"。

No. PI006-A-L-SD-4.1

TITLE	SNT-6A(H)-A -Land Recommendation	
No.	PI006-A-L-SD-4.1	
ANGLE		
UNIT	mm	
ABLIC Inc.		

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